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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/757,925
				Filing Date	January 16, 2004
				First Named Inventor	Craig C. HANSEN, et al.
				Group Art Unit	2181
				Examiner Name	TSAI, HENRY
Sheet	1	of	2	Attorney Docket Number	43876-158

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-5,132,898	07-21-1992	Sakamura et al.	
		US-5,410,682	04-25-1995	Sites et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number 4- Kind Code <sup>5</sup> (if known)				

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Diefendorff et al., "The PowerPC User Instruction Set Architecture," <i>IEEE Micro</i> , no.5, 30-41 (Oct. 1994) [XP000476678]	
		Greenley et al., "UltraSPARC <sup>TM</sup> : The Next Generation Superscaler 64-bit SPARC," <i>IEEE</i> , 442-51 (1995) [XP000545452]	
		Laudon et al., "Interleaving: A Multithreading Technique Targeting Multiprocessors and Workstations," <i>ACM Sigplan Notices</i> , no. 11, 308-18 (Nov. 29, 1994) [XP000491743]	
		"MC88110 RISC Microprocessor User's Manual, Second Edition," Motorola, Inc. (1990), Sections 1 through 3 (148 pages) [XP002474804]	
		Sato et al., "Multiple Instruction Streams in a Highly Pipelined Processor," <i>IEEE</i> , 182-89 (1990) [XP010021363]	
		Yamamoto et al., "Performance Estimation of Multistreamed, Superscaler Processors," <i>IEEE</i> , 195-204 (1994) [XP010096943]	
		Zhou et al., "MPEG Video Decoding with the UltraSPARC Visual Instruction Set," <i>IEEE</i> , 470-75 (1995) [XP002472254]	
		P27838EP-D1-PCT Extended European Search Report dated May 2, 2008 in Application No. / Patent No. 07111352.6-1243	
		P27838EP-D4-PCT Extended European Search Report dated May 8, 2008 in Application No. / Patent No. 07111349.2-1243	
		P27838EP-D5-PCT Extended European Search Report dated April 23, 2008 in Application No. / Patent No. 07111344.3-1243	
		P27838EP-D6-PCT Partial European Search Report dated April 1, 2008 in Application No. / Patent No. 07111348.4-1243	
		P27838EP-D7-PCT Extended European Search Report dated March 10, 2008 in Application No. / Patent No. 07111473.0-1243	
		P27838EP-D8-PCT Extended European Search Report dated May 27, 2008 in Application No. / Patent No. 07111476.3-1243	
		P27838EP-D9-PCT Extended European Search Report dated March 26, 2008 in Application No. / Patent No. 07111480.5-1243	

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